

<b>Notice of References Cited</b>	Application/Control No. 09/487,483	Applicant(s)/Patent Under Reexamination SHIBA ET AL.	
	Examiner Michael J. Simitoski	Art Unit 2134	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-5,379,243 A	01-1995	Greenberger et al.	708/492
	C	US-6,049,815 A	04-2000	Lambert et al.	708/492
	D	US-6,178,436 B1	01-2001	Blake et al.	708/492
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	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Drescher, Wolfram et al. "VLSI Architecture for Datapath Integration of Arithmetic Over GF(2 <sup>m</sup> ) on Digital Signal Processors", 1997 IEEE.
	V	Paar, Christof et al. "Efficient Multiplier Architectures for Galois Fields GF(2 <sup>4n</sup> )", 1998 IEEE.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.